IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	Andrew Ho, Vladimir Stojanovic, Bruno W. Garlepp, and Fred F. Chen		
Assignee:	Rambus Inc.		
Title:	"MARGIN TEST METHODS AND CIRCUITS"		
Serial No.:	Unknown	Filed:	March 31, 2004
Examiner:	Unknown	Tel:	Unknown
Docket No.:	RA290.CIP1.US	Art Unit:	Unknown

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
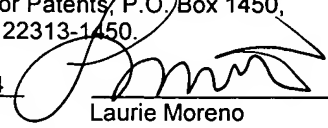Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. §1.56, §1.97 and §1.98, Applicants bring the 11 documents listed on the enclosed form PTO-1449 to the Examiner's attention in the above-captioned application. Citation of the listed documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant application;

2. a representation that a search has been made; or

3. an admission that the information cited is, or is considered to be, material to patentability as defined in §1.56(b).
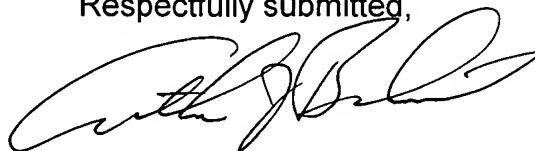
A copy of document "D" and "K" are enclosed. Copies of the other documents listed are not submitted because they were considered in an earlier application (10/441,461, filed 05/20/2003) which is relied upon for an earlier filing date under 35 U.S.C. §120.

<table>
<tr><td>
I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail No.: ER 264470356 US and is addressed to: Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

March 31, 2004
Date   Laurie Moreno
</td><td>
Respectfully submitted,

Arthur J. Behiel
Attorney for Applicants
Reg. No. 39,603
</td></tr>
</table>

U.S. Department of Commerce, Patent and Trademark Office	Serial No.: Unknown
	Filing Date: March 31, 2004
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	First Named Inventor: Andrew Ho
"MARGIN TEST METHODS AND CIRCUITS"	Group Art Unit: Unknown
	Examiner Name: Unknown
Express Mail Receipt No. ER 264470356 US	Attorney Docket No.: RA290.CIP1.US

U.S. Patent / Patent Application Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
	A	5,331,663	07/19/94	Kurokami	375	14	
	B	6,493,394B2	12/10/02	Tamura et al.	375	257	
	C	10/441,461	Filed: 05/20/03	Chen, Fred F.			

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	D	STOJANOVIC, Vladimir et al., "Modeling and Analysis of High-Speed Links." Research supported by the MARCO Interconnect Focus Center and Rambus, Inc. 2003. 8 pages.
	E	KIRAN, Rajarshi Gupta, et al., "Computationally Efficient Version of the Decision Feedback Equalizer." September 1998. 4 Pages.
	F	DEGEN, Christoph M. Degen, et al., "Comparative Study of Efficient Decision-Feedback Equalization Schemes for MIMO Systems." Presented in February 2002. 7 Pages. Institute of High Frequency Technology, RWTH Aachen, Germany.
	G	KOETER, John, "What's an LFSR?" Texas Instruments, SCTA036A. December 1996. 2 cover pages, pages iiii-iv, pages 1-7.
	H	EREIGEJ, Heider N. et al., "Intersymbol Interference and Timing Jitter Measurements in a 40-Gb/s Long-Haul Dispersion-Managed Soliton System." IEEE Photonics Technology Letters, Vol. 14, No. 3, March 2002. Pages 343-345.
	I	AUGEH, Malik D. et al., "Decision-Feedback Equalization of Pulse-Position Modulation on Measured Nondirected Indoor Infrared Channels." IEEE Transactions on Communications, Vol. 47, No. 4, April 1999. Pages 500-503.
	J	ALFKE, Peter, "Efficient Shift Registers, LFSR Counters, and Long Pseudo-Random Sequence Generators." Xilinx Application Note, XAPP 052, (version 1.1), July 7, 1996. 6 pages.
	K	"SHMOO User's Guide." 500 Semiconductor Production Test System. Copyright © 1998-2001, LMO Test Systems, Inc. Last Revised: April, 2001. Cover, Table of Contents, pages 1-28.

Examiner	Date Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.